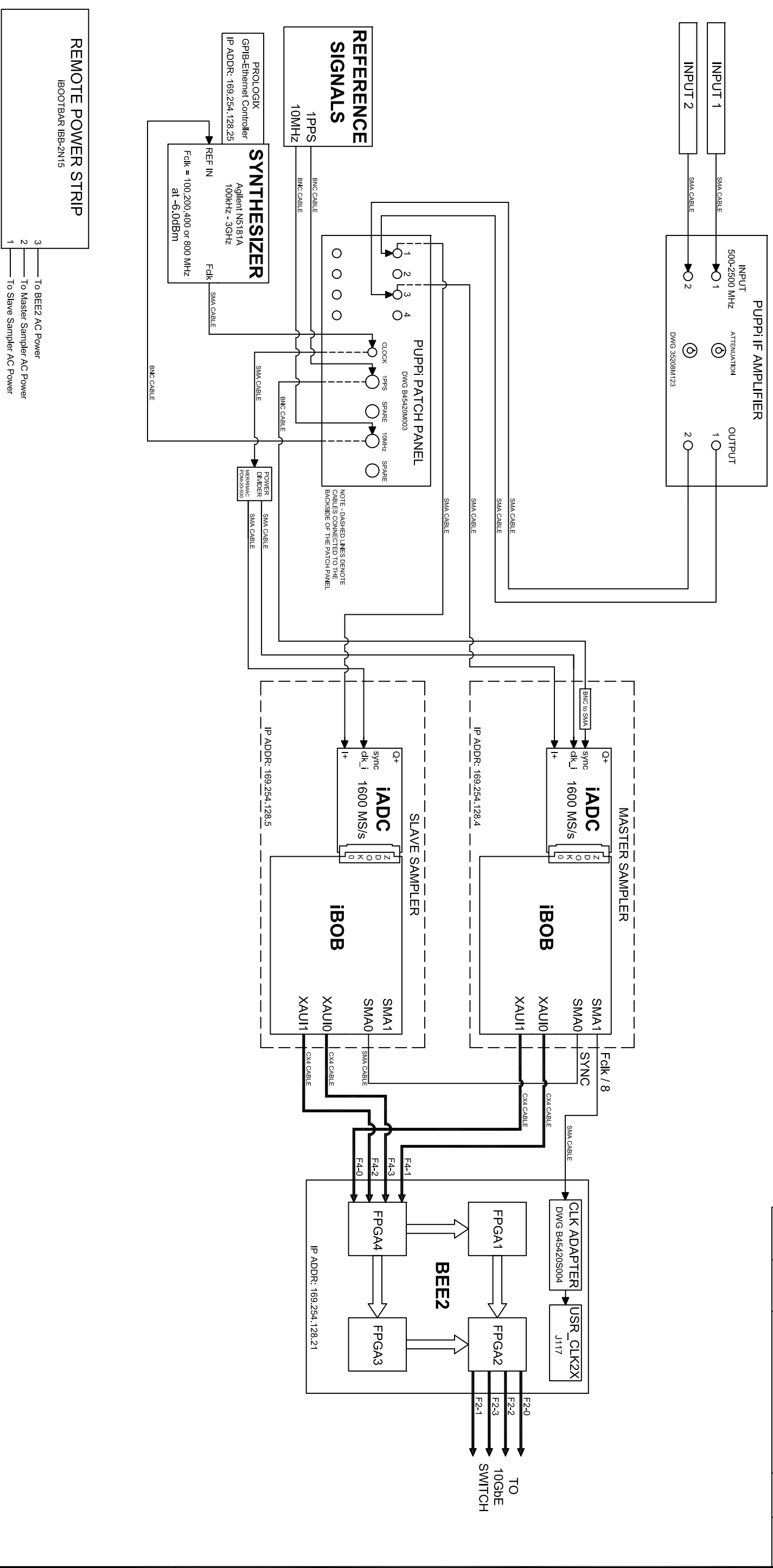


REVISIONS				
REVISION	MADE BY	DESCRIPTION	ZONE	DATE
A	JMR	CORRECTED XAUI CONNECTIONS	ALL	02/14/12



**REMOTE POWER STRIP**  
IBOOTBAR IBB-2N15

3 — To BEE2 AC Power  
2 — To Master Sampler AC Power  
1 — To Slave Sampler AC Power

**REFERENCE SIGNALS**  
1PPS 10MHz

**SYNTHESIZER**  
Agilent NS181A  
100KHz - 3GHz  
Fclk = 100,200,400 or 800 MHz  
at -6,0dBm

PROLOGIX  
GPIB-Ethernet Controller  
IP ADDR: 169.254.128.25

**PUPPI PATCH PANEL**  
DWG B45420M003

CLOCK 1PPS SPARE 10MHz SPARE

NOTE - DASHED LINES DENOTE CABLES CONNECTED TO THE BACKSIDE OF THE PATCH PANEL

**MASTER SAMPLER**  
IP ADDR: 169.254.128.4

Q+ sync dkl j +

**!ADC**  
1600 MS/s

SMA1 SMA0

XAUI1 XAUI0

**SLAVE SAMPLER**  
IP ADDR: 169.254.128.5

Q+ sync dkl j +

**!ADC**  
1600 MS/s

SMA1 SMA0

XAUI1 XAUI0

**CLK ADAPTER**  
DWG B45420S004

USR\_CLK2X J117

**FPGA1** ↔ **FPGA2** ↔ **FPGA3** ↔ **FPGA4**

**BEE2**

F2-0 F2-2 F2-3 F2-1

TO 10GbE SWITCH

**NATIONAL RADIO ASTRONOMY OBSERVATORY**  
P.O. BOX 2, GREEN BANK, WEST VIRGINIA, 24944

PROJECT/TITLE: PUPPI  
BLOCK DIAGRAM

DESIGNED: MANY DRAWN: JMR CHECKED: FINISH

SCALE: MATERIAL: SHEET: 1 OF 1 REVISION: A

FILE: \ARCHIVE\45420\K004

DATE: 07/21/11

DRAWING NO.: B45420K004