



To Timing Gen. Drifted Clock  
20MHz  
+13dBm  
(R/P)

DDS2  
20MHz  
+13dBm  
(F/P)

20MHz  
+13dBm  
(R/P to Sinc Filter)

20MHz  
+13dBm  
(R/P to FN Code)

20MHz  
+13dBm  
(R/P to Upconverter)

20MHz  
+13dBm  
(R/P to Down Converter)

DDS1  
20MHz  
+13dBm  
(F/P)

ARECIBO OBSERVATORY		
BY : BILL SISK		
FILE : DDSCTRL11.SCH		
Title		
DDS CONTROLLER		
Size	Document Number	Rev
B	RF	
Date:	Friday, March 11, 2005	Sheet 0 of 0