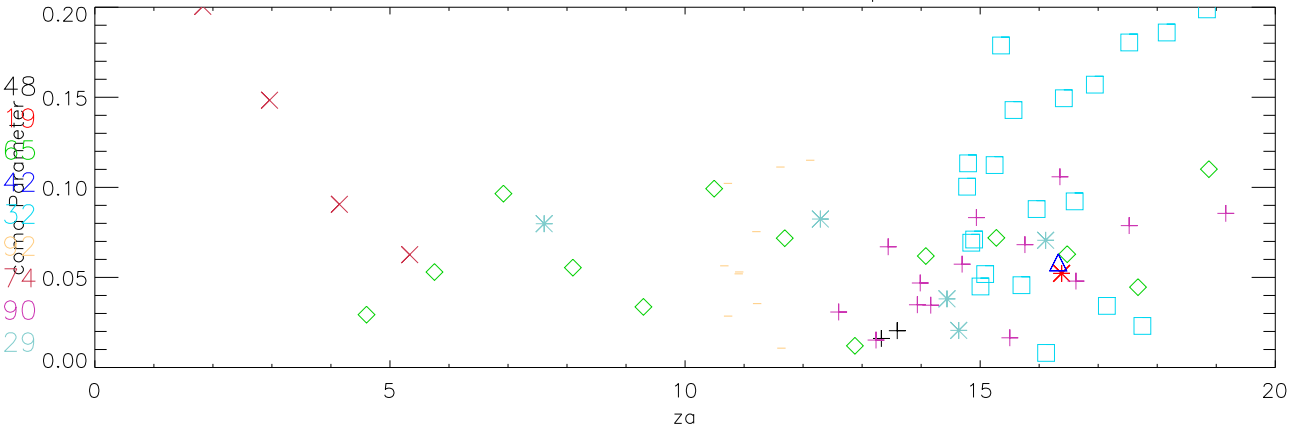
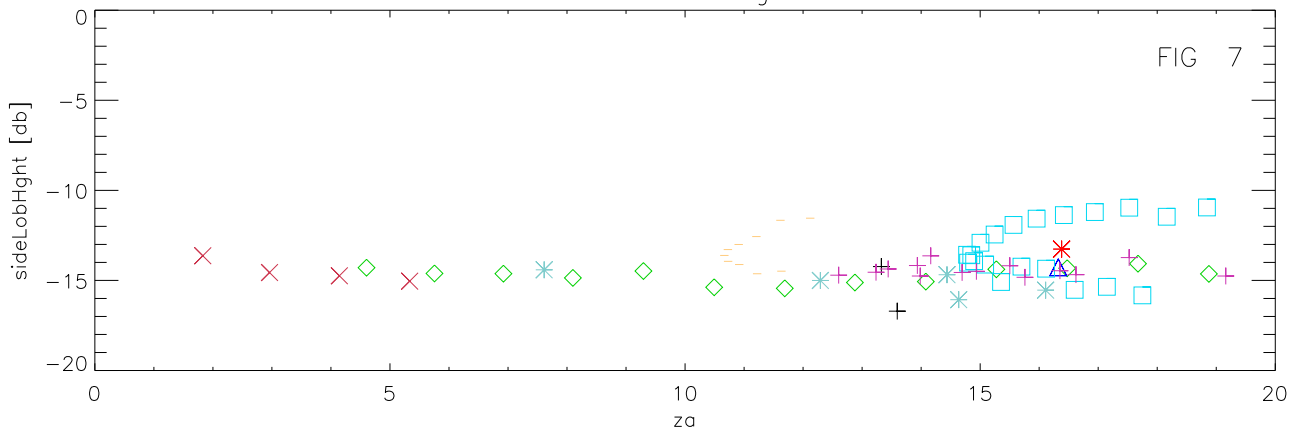


01oct08 to 31oct08 6600 Mhz 70 pnts. Coma

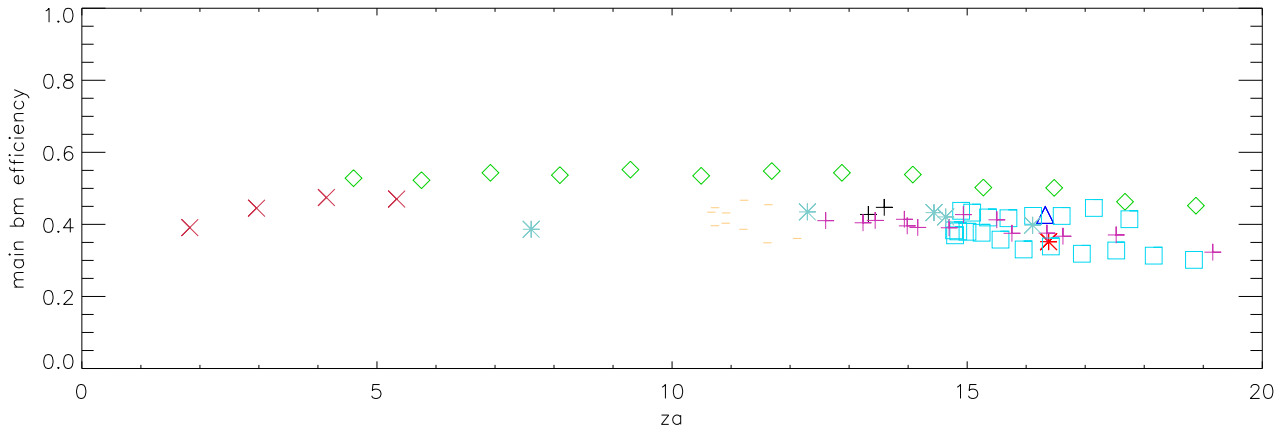
- + B0340+048
- * B0500+019
- ◇ B0518+119
- △ B0534+340
- B0732+347
- B0858+210
- × B1645+174
- + B1829+290
- * B1857+129



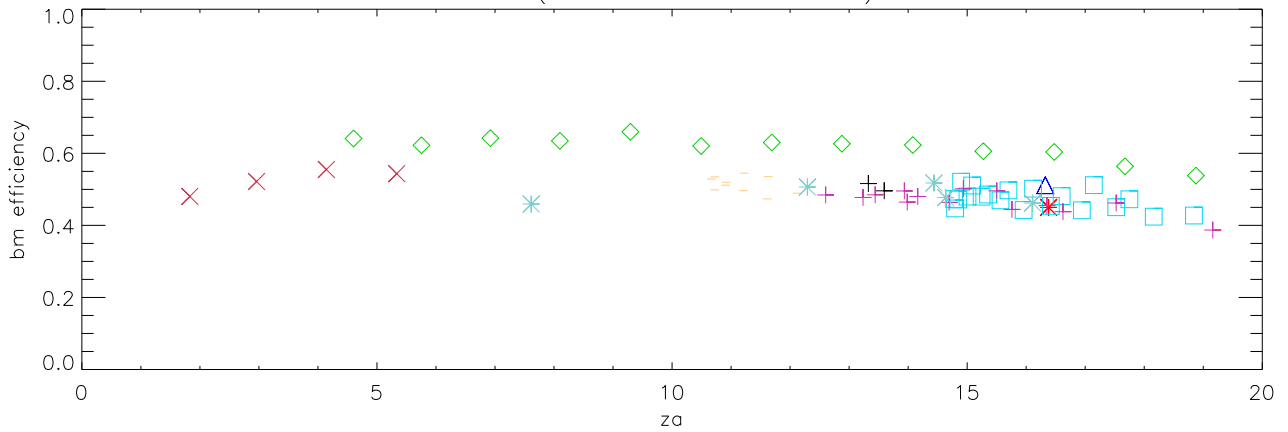
SideLobe Height



Eta MainBm

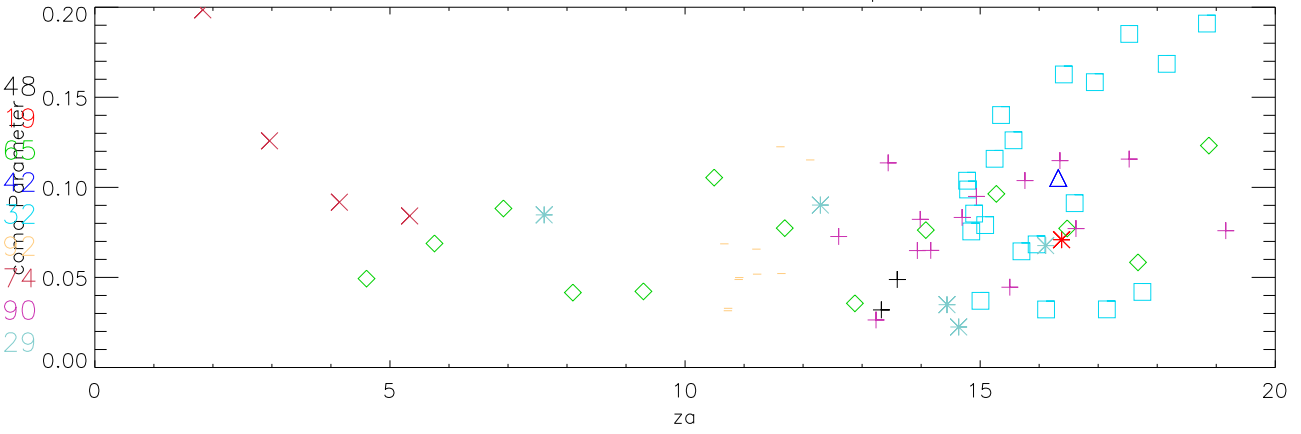


Eta (MainBm + 1st SideLobe)

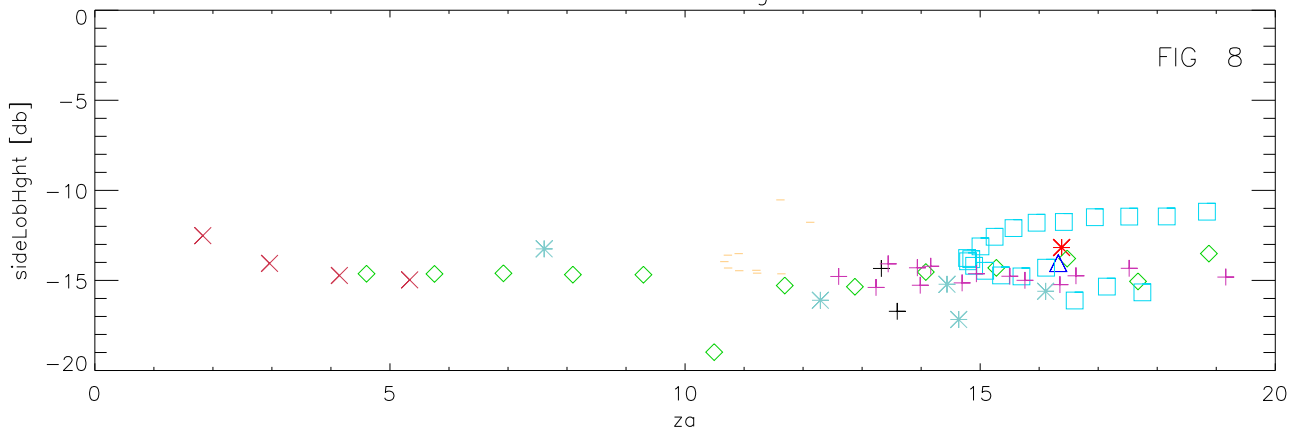


01oct08 to 31oct08 6900 Mhz 70 pnts. Coma

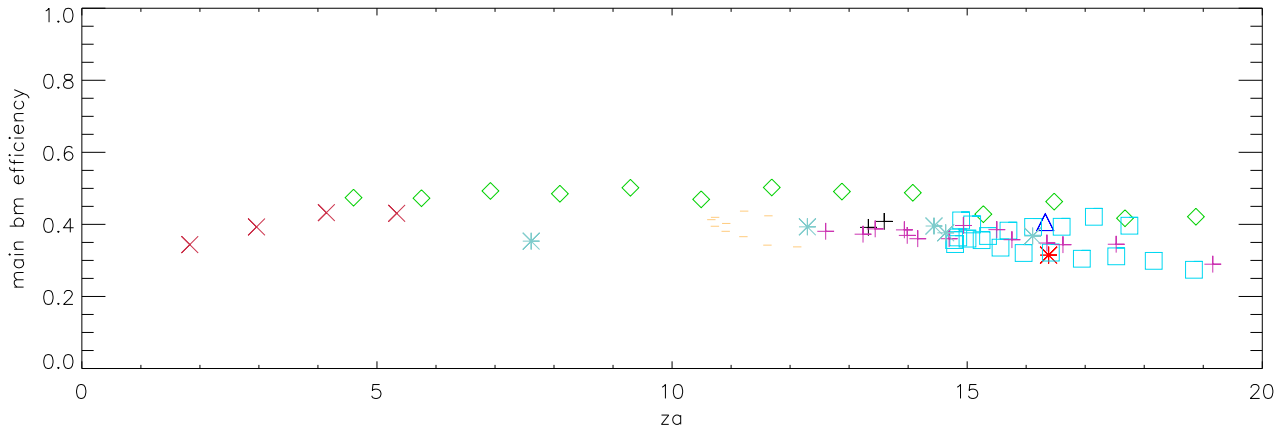
- + B0340+048
- * B0500+019
- ◇ B0518+110
- △ B0534+347
- B0732+347
- B0858+210
- × B1645+174
- + B1829+290
- * B1857+129



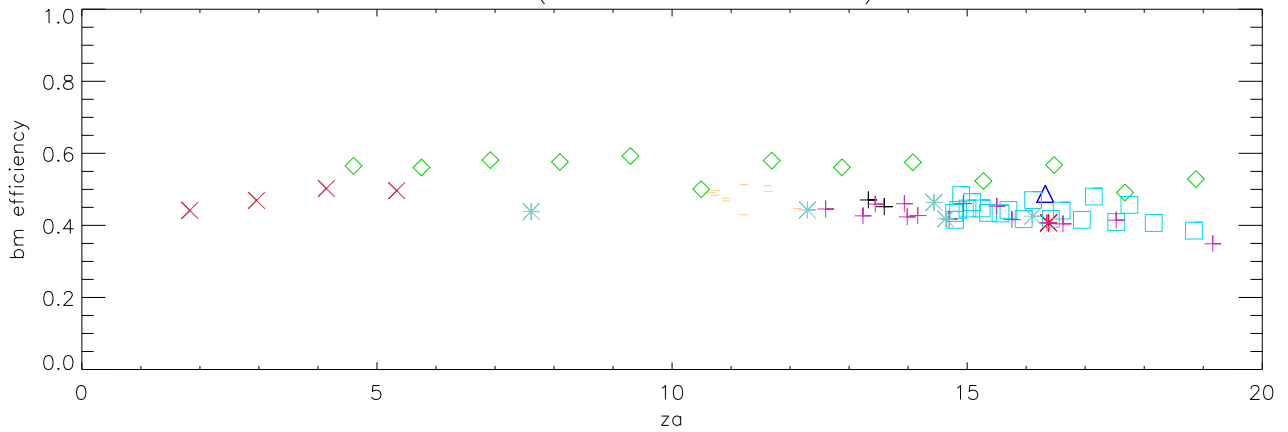
SideLobe Height



Eta MainBm

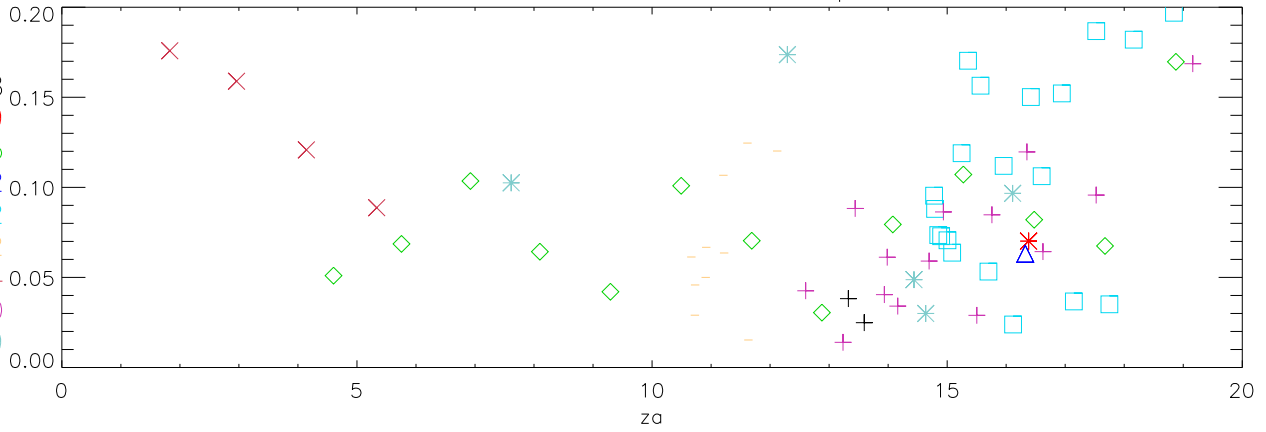


Eta (MainBm + 1st SideLobe)

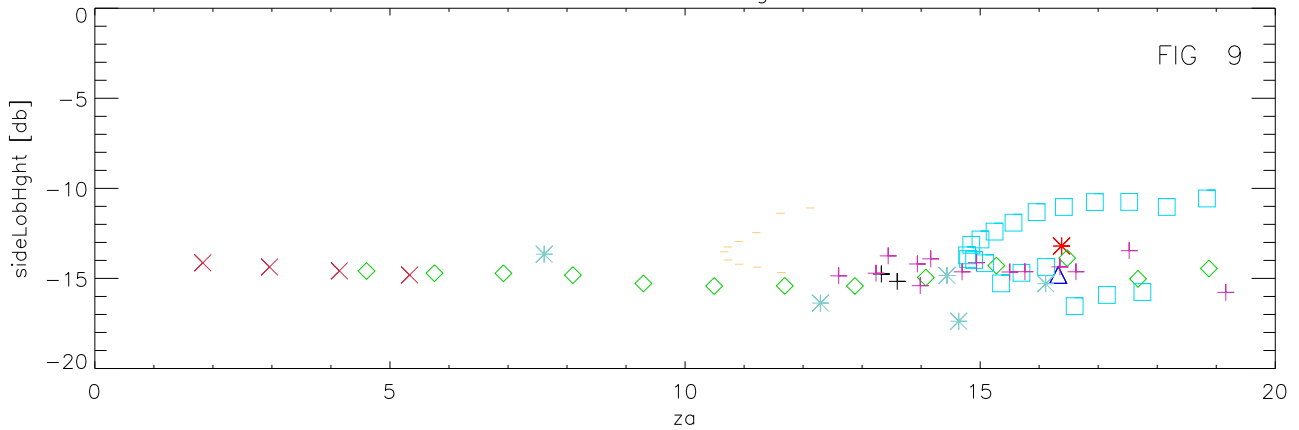


01oct08 to 31oct08 7200 Mhz 70 pnts. Coma

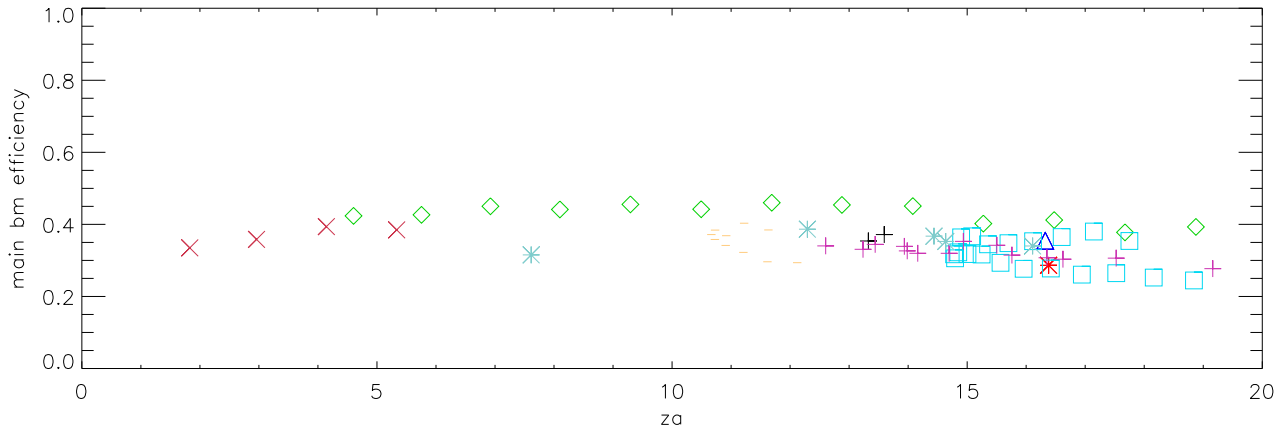
- + B0340+048
- * B0500+019
- ◇ B0518+109
- △ B0534+340
- B0732+347
- B0858+212
- × B1645+174
- + B1829+290
- * B1857+129



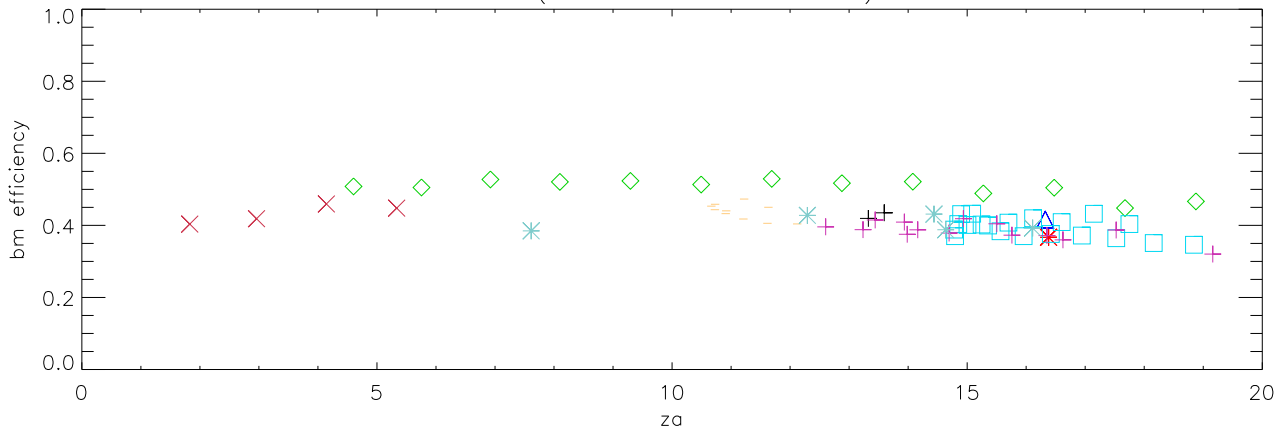
SideLobe Height



Eta MainBm

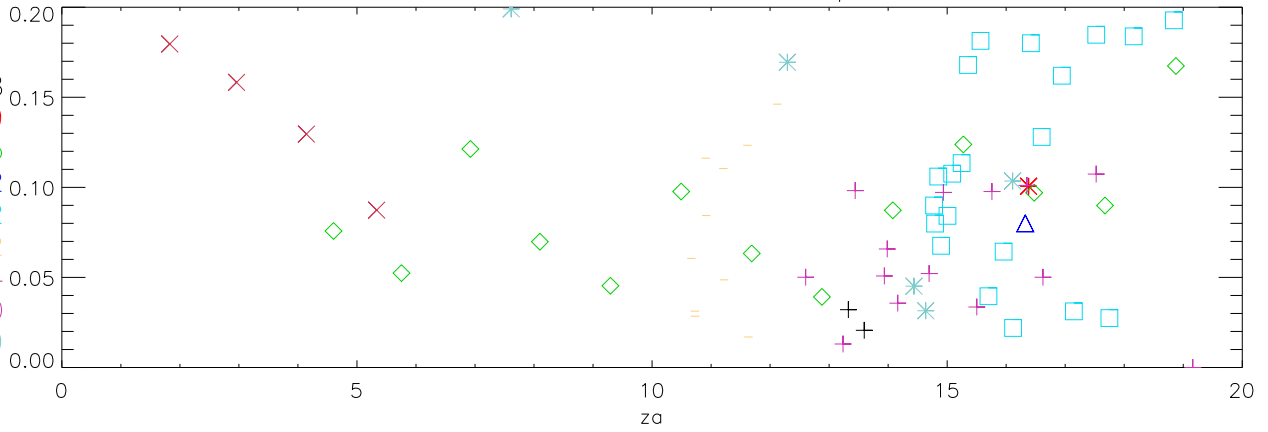


Eta (MainBm + 1st SideLobe)

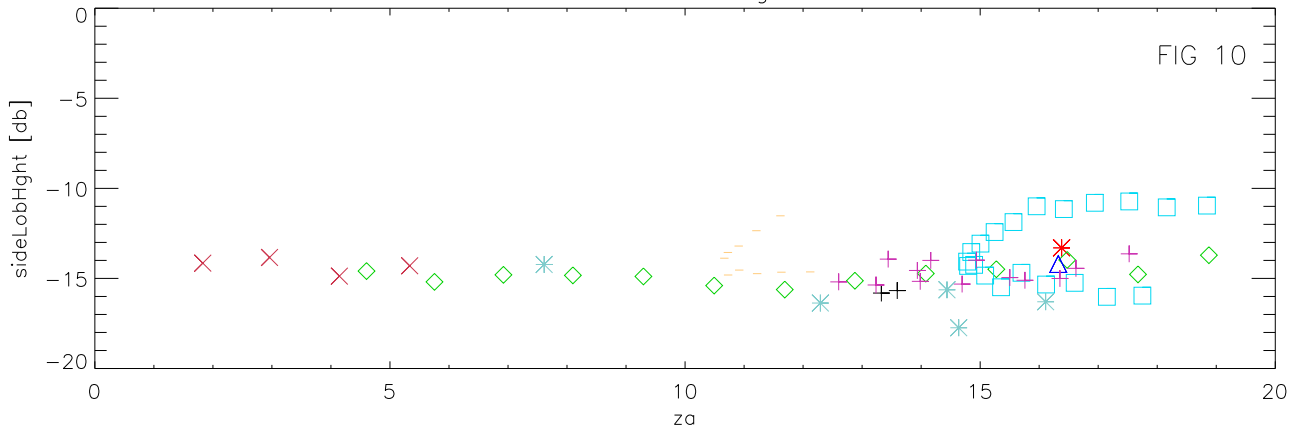


01oct08 to 31oct08 7400 Mhz 70 pnts. Coma

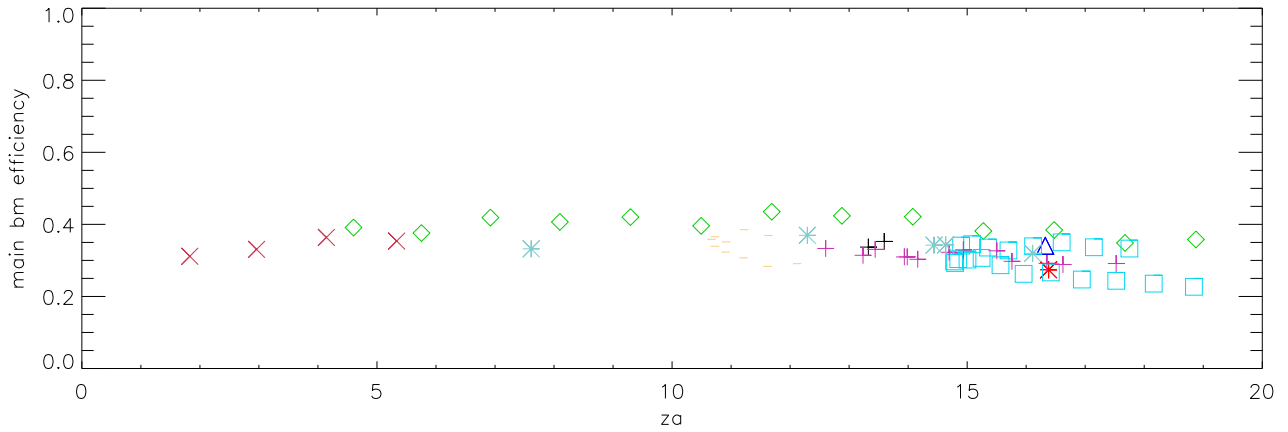
- + B0340+048
- * B0500+019
- ◇ B0518+109
- △ B0534+340
- B0732+347
- B0858+210
- × B1645+174
- + B1829+290
- * B1857+129



SideLobe Height



Eta MainBm



Eta (MainBm + 1st SideLobe)

